PATENT COOPERATION TREATY

PCT

INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)

Applicant's or agent's file reference 09150-013W01	FOR FURTHER ACTION	see Notification of Transmittal of International Search Report (Form PCT/ISA/220) as well as, where applicable, item 5 below.
International application No. PCT/US01/29324	International filing date *(day/month/year)* 18 SEPTEMBER 2001	(Earliest) Priority Date *(day/month/year)* 18 SEPTEMBER 2000

Applicant SITARA NETWORKS, INC.

This international search report has been prepared by this International Searching Authority and is transmitted to the applicant according to Article 18. A copy is being transmitted to the International Bureau.

This international search report consists of a total of **3** sheets.

- [X] It is also accompanied by a copy of each prior art document cited in this report.

1. Basis of the report

    a. With regard to the **language,** the international search was carried out on the basis of the international application in the language in which it was filed, unless otherwise indicated under this item.

- [ ] the international search was carried out on the basis of a translation of the international application furnished to this Authority (Rule 23.1(b)).

    b. With regard to any **nucleotide and/or amino acid sequence** disclosed in the international application, the international search was carried out on the basis of the sequence listing:

- [ ] contained in the international application in written form.

- [ ] filed together with the international application in computer readable form.

- [ ] furnished subsequently to this Authority in written form.

- [ ] furnished subsequently to this Authority in computer readable form.

- [ ] the statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the

- [ ] the statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished.

2. [ ] **Certain claims were found unsearchable** (See Box I).

3. [ ] **Unity of invention is lacking** (See Box II).

4. With regard to the **title,**

- [X] the text is approved as submitted by the applicant.

- [ ] the text has been established by this Authority to read as follows:

5. With regard to the **abstract,**

- [ ] the text is approved as submitted by the applicant.

- [X] the text has been established, according to Rule 38.2(b), by this Authority as it appears in Box III. The applicant may, within one month from the date of mailing of this international search report, submit comments to this Authority.

6. The figure of the **drawings** to be published with the abstract is Figure No. __1__

- [ ] as suggested by the applicant.
- [X] because the applicant failed to suggest a figure.
- [ ] because this figure better characterizes the invention.

- [ ] None of the figures.

Form PCT/ISA/210 (first sheet) (July 1998)*

Box III   TEXT OF THE ABSTRACT (Continuation of item 5 of the first sheet)

The technical features mentioned in the abstract do not include a reference sign between parentheses (PCT Rule 8.1(d)).

NEW ABSTRACT

A distributed quality-of-service system (100) makes use of a distributed architecture to achieve very high throughput and availability in which a number of separate processors (110A,110B,110C) are loosely coupled in a scalable communication architecture. The computational load required to implement an approach that is similar to that of Floyd and Jacobson is distributed among the processors (110A,110B,110C) without requiring the processors to be tightly coupled. A high data rate flow is split so that each processor (110A,110B,110C) receives a portion (140A,140B,140C) of the traffic passing through the system. The processors (110A,110B,110C) implement separate class-based queuing and link fairness algorithms. The output flows (130A,130B,130C) of each of the processors (110A,110B,110C) are combined (150) to form the overall output flow (132). The processors (110A,110B,110C) exchange data (120) from time to time in order to achieve an approximate link fairness on the overall output flow without requiring the separate algorithms implemented on each of the processors (110A,110B,110C) to be tightly coupled.